```
************************************************************

Stage:  Grism

I1 = 11110001
I2 = 01100011
O2 = 00000000

I1 Bit 1 set.       Cell retainer is LATCHED.
I2 Bit 1 not set.   Cell retainer is NOT UNLATCHED.
I1 Bit 2 set.       Transport is STOWED.
I1 Bit 3 set.       Carousel cover is CLOSED.
I1 Bit 4 set.       Carousel spare #1 is 1.
I1 Bit 5 not set.   Transport is NOT DEPLOYED
I1 Bit 6 not set.   Cell is NOT in position according to Switch #1.
I1 Bit 7 not set.   Cell is NOT in position according to Switch #2.
I1 Bit 8 set.       Transport CLAMP is OFF.

I2 Bit 4 not set.  Carousel brake is on.  Carousel will not move.

     *** BOTH limit switches are set ***

************************************************************

punaluu{lris}80: 
```